

IMPLEMENTATION ON CLOCK GENERATION OF 25MHZ USING TRANSLATION BUFFER INTERFACE FROM ECL-CMOS

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ABSTRACT

The propitious crystal oscillator is considered as utmost important in a high speed application, since it can dispense the clock reference for the entire clock distribution system. In the proposed paper, a circuit principle is presented to realize an extremely fast ECL to CMOS logic conversion, where the possibility of generating clock frequency of 25MHz is obtained instead of a crystal oscillator. SPICE is found as a mixed use circuit analyzer that simulates electronic circuits and can perform various analysis on electronic circuits. By employing PSPICE, the analysis of the circuit has been proffered in this paper. Also, this paper describes about the significance of crystal oscillator which can be designed using emergent technology described for high speed application.

KEYWORDS: *Crystal Oscillator, Clock Frequency, ECL to CMOS Logic Converter, PSPICE Software*

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